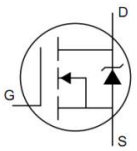
● General Description

It combines trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

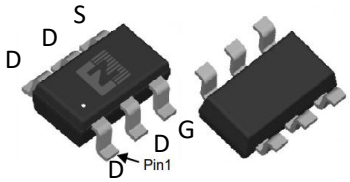
● Product Summary



V_{DS}= 60V
$R_{DS(ON)}$ = 50mΩ
I_D= 4.5A

● Features

■AEC-Q101 Qualified
■Low $R_{DS(ON)}$ to minimize conductive loss
■Low Gate Charge for fast switching
■Low Thermal resistance



SOT-23-6

●Application

■BLDC Motor driver
■DC-DC
■Battery protection

●Ordering Information:

Part NO.	ZMA500N06U
Marking	500N06
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

●Absolute Maximum Ratings (T_C=25℃)

Parameter	Symbol	Conditions	Value	Unit
Drain-Source Voltage	V_{DS}		60	V
Gate-Source Voltage	V_{GS}		±20	V
Continuous Drain Current	I_D	T_C=25℃	4.5	A
	I_D	T_C=75℃	3	A
	I_D	T_C=100℃	3	A
Pulsed Drain Current	I_{DM}	Pulsed; t_p ≤ 10 μs; T_{mb} = 25 °C;	18	A
Total Power Dissipation	P_D	T_C=25℃	2	W
Total Power Dissipation	P_D	T_A=25℃	0.7	W
Operating Junction Temperature	T_J		-55 to +150	℃
Storage Temperature	T_{STG}		-55 to +150	℃
Single Pulse Avalanche Energy	E_{AS}	L=0.1mH, V_{GS}=10V, Rg=25Ω,	5	mJ
		L=0.5mH, V_{GS}=10V, Rg=25Ω,	10.5	mJ
ESD Level (HBM)	CLASS 1B			

●Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}		-	60	°C/W
Thermal resistance, junction-ambient①	R_{thJA}		-	180	°C/W
Soldering temperature	Tsold		-	260	°C

●Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS} =0V, I_D =250uA	60			V
Gate Threshold Voltage	$V_{GS(TH)}$	V_{GS} =V_{DS}, I_D =250uA	1.3	1.7	2.5	V
Drain-Source Leakage Current	I_{DSS}	V_{GS}=0V, V_{DS}= 60V			1.0	uA
Gate- Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS} = 0V			100	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D= 5A		50	65	mΩ
		V_{GS}=4.5V, I_D= 3A		70	90	mΩ
Forward Transconductance	g_{FS}	V_{DS} =5V, I_{SD} = 10A		5		s
Diode Forward Voltage	V_{FSD}	V_{GS} =0V, I_{SD} = 5A			1.3	V

●Dynamic characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	Ciss	f = 1MHz, V_{DS}=25V	-	700	-	pF
Output capacitance	Coss		-	33	-	
Reverse transfer capacitance	Crss		-	19	-	
Gate Risistance	Rg	f = 1MHz	-	1		Ω
Total gate charge	Qg	V_{DD} = 15V, I_D = 20A, V_{GS} = 10V	-	11	-	nC
	Qg (4.5v)		-	5.5	-	
Gate - Source charge	Qgs		-	2.5	-	
Gate - Drain charge	Qgd		-	1.5	-	
Turn-ON Delay time	$t_{D(on)}$	V_{GS}=10V,V_{DS}=15V, R_G =3.3Ω, I_D =20A	-	2	-	ns
Turn-ON Rise time	t_r		-	3	-	ns
Turn-Off Delay time	$t_{D(off)}$		-	10	-	ns
Turn-Off Fall time	t_f		-	7	-	ns
Reverse Recovery Time	t_{RR}	V_{DD}=20V, dI_S/dt = 100A/us, I_S=20A	-	30	-	ns
Reverse Recovery Charge	Q_{RR}		-	28	-	nC

Fig.1 Gate-Charge Characteristics

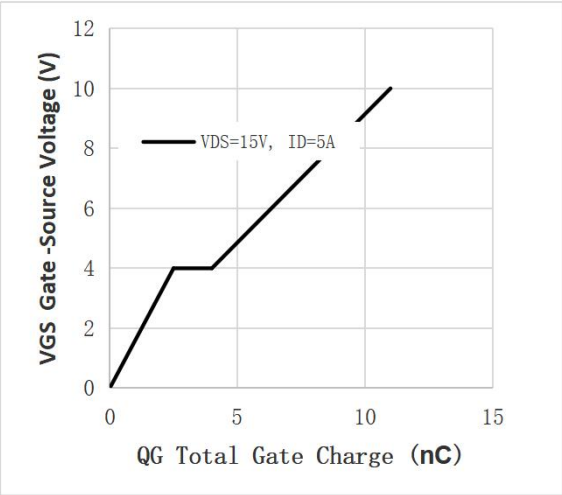


Fig.2 Capacitance Characteristics

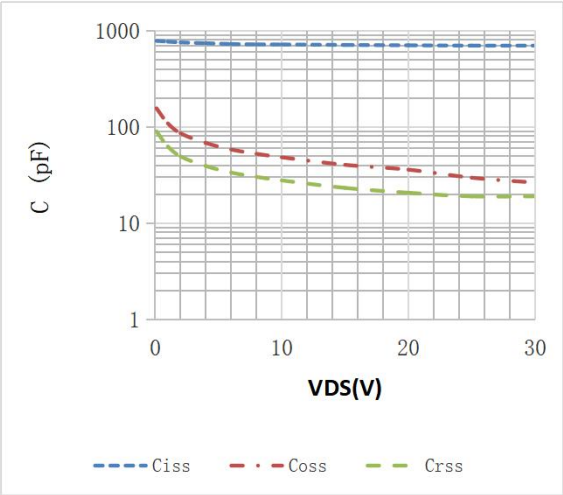


Fig.3 Power Dissipation

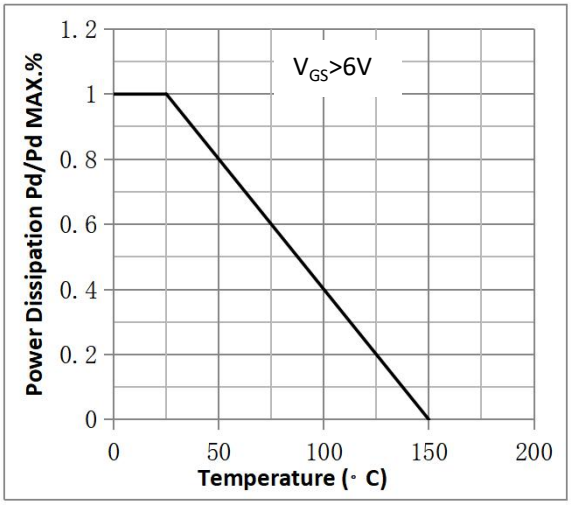


Fig.4 Typical output Characteristics

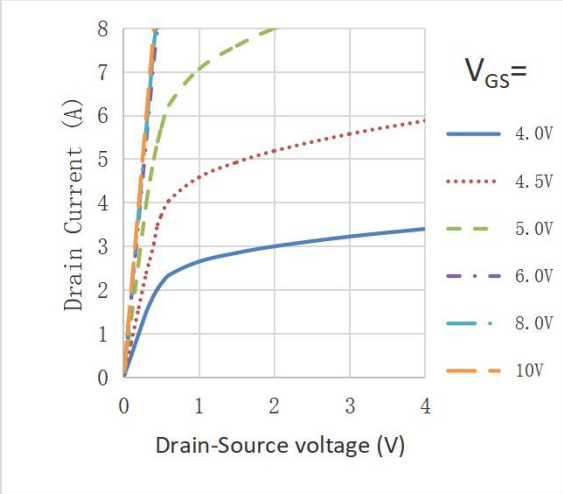


Fig.5 Threshold Voltage V.S Junction Temperature

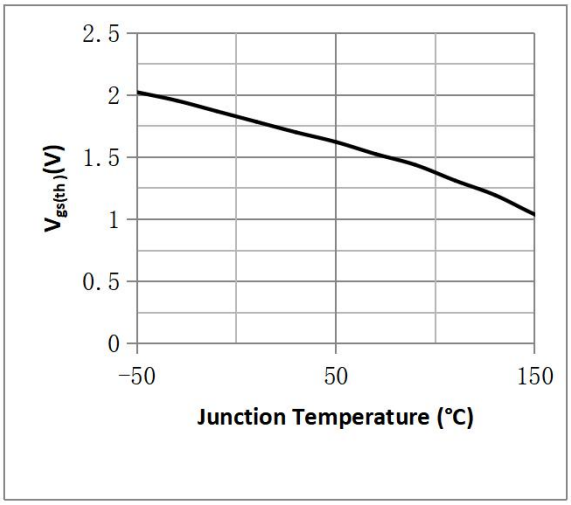


Fig.6 Resistance V.S Drain Current

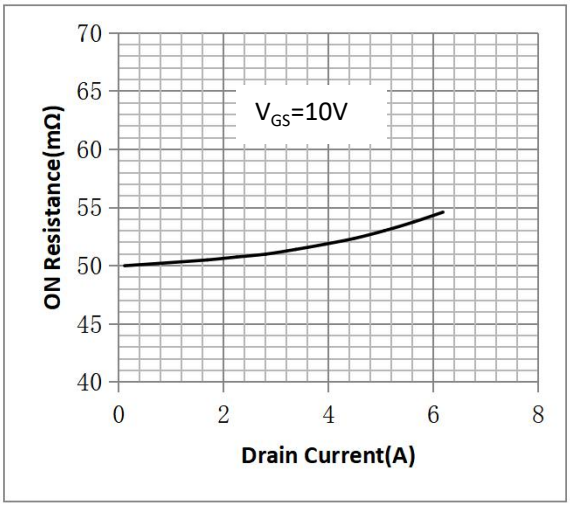


Fig.7 On-Resistance VS Gate Source Voltage

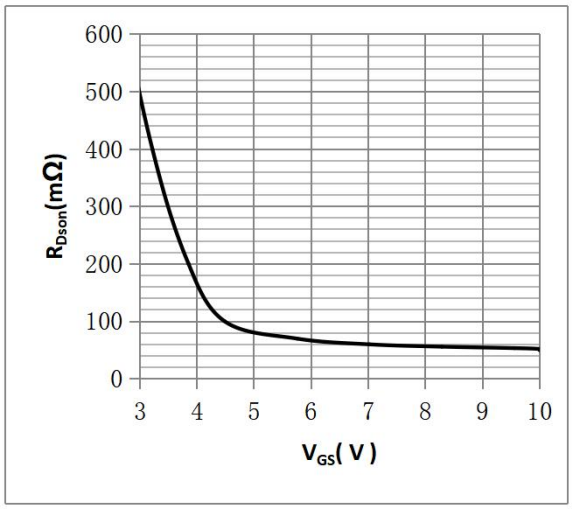


Fig.8 On-Resistance V.S Junction Temperature

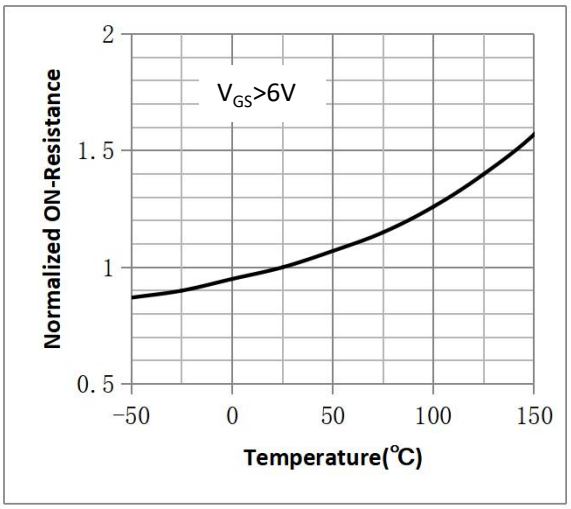


Figure 9. Diode Forward Voltage vs. Current

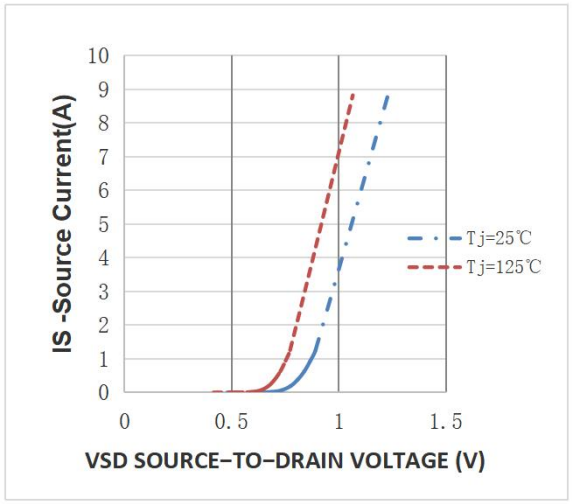


Figure 10. Transfer Characteristics

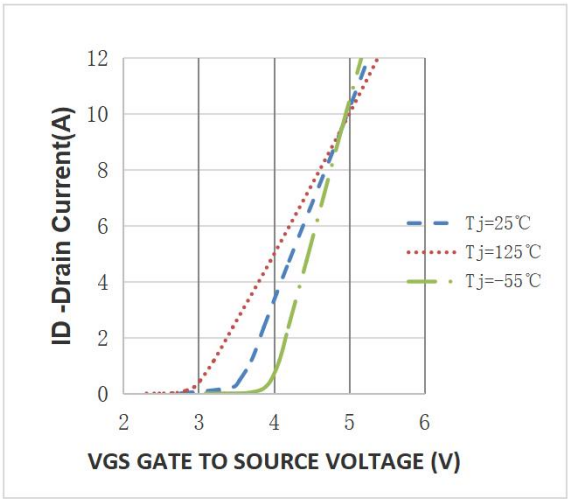


Fig.11 Safe Operating Area

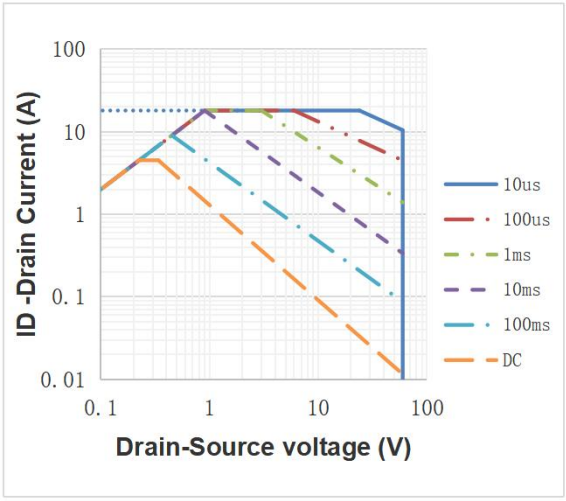
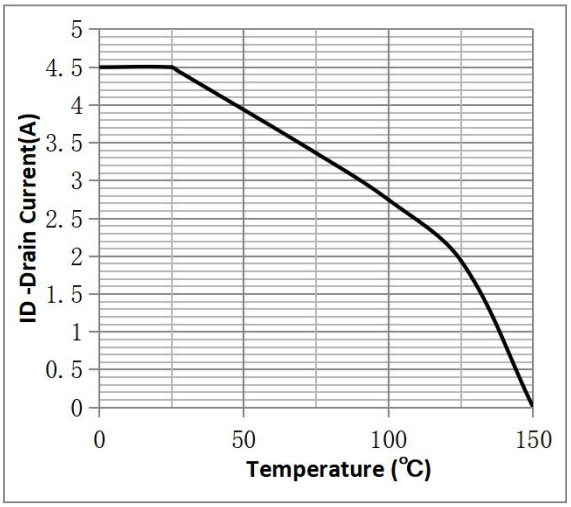
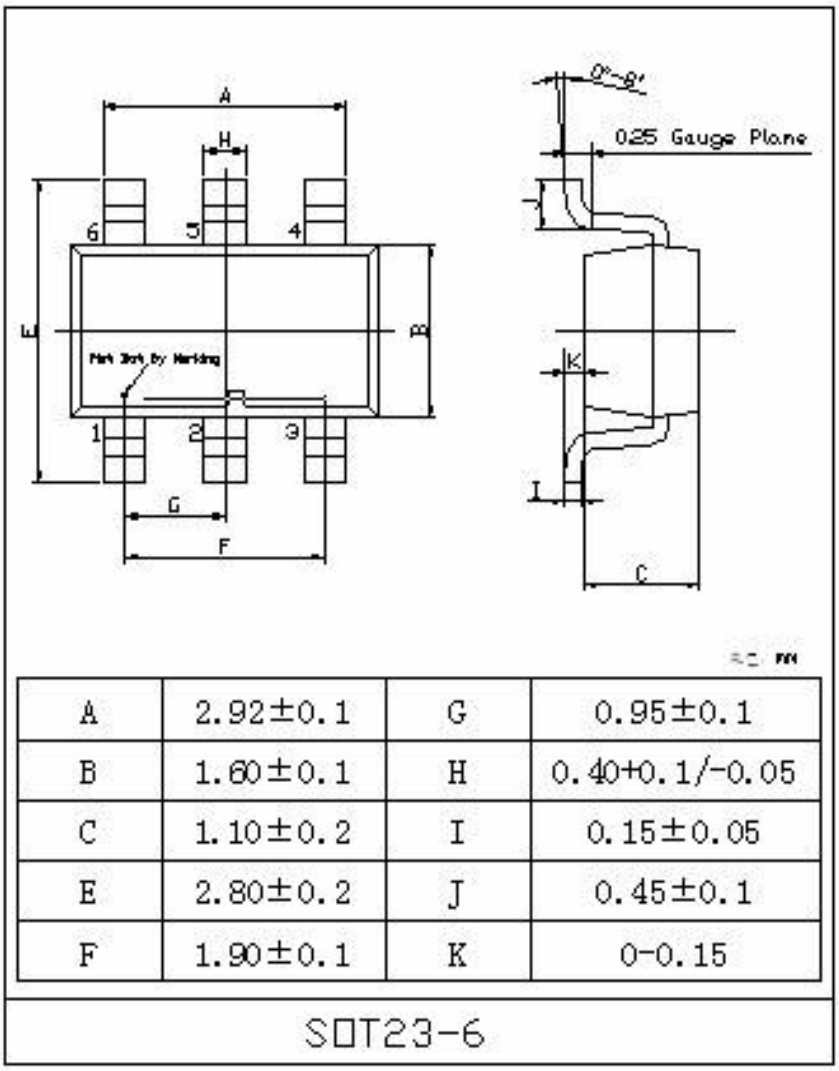


Fig.12 ID vs. Junction Temperature②



●SOT-23-6 Package Outline



A	2.92±0.1	G	0.95±0.1
B	1.60±0.1	H	0.40+0.1/-0.05
C	1.10±0.2	I	0.15±0.05
E	2.80±0.2	J	0.45±0.1
F	1.90±0.1	K	0-0.15

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Note:

① Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate；

② Practically the current will be limited by PCB, thermal design and operating temperature. VGS=10V.

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Revision History

Version	Date	Change
A	2021.12.10	
B	2022.9.5	1.Add Reach,HF figure,2.ID modify